



School of Nano Science



IPM Condensed Matter &
Statistical Physics Group

Weekly Seminar

Designing QHC simple logic gates, half-adder and majority gates circuit on a Si(100)–H surface

Invited Speaker:

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Abstract:

Belonging to the Quantum Hamiltonian Computing (QHC) branch of quantum control [1-2], atomic-scale Boolean logic gates (LGs) with two inputs - one output (OR, NOR, AND, NAND, XOR, NXOR) and - two outputs (half-adder circuit) were designed on a Si(100)-(2× 1)–H surface following the experimental realization of a QHC NOR gate [3] and the formal design of an half-adder with 6 quantum states in the calculating block [4]. The logical inputs are determined by two nearest neighbor crossing surface Si dangling bonds, which can be, for example, activated by adding or extracting two hydrogen atoms per input. QHC circuit design rules together with semi-empirical full valence K-ESQC transport calculations were used to determine the output current intensity of the designed LGs when interconnected to the metallic nano-pads by surface atomic-scale wires. We represent the process also for the measurements of the Rabi-Heisenberg oscillation of the our designed calculating block explaining the quantum to classical information conversion [5]. Our calculations demonstrate that the proposed devices can reach a “0” to “1” logical output ratio up to 10 000 for a running current in the 0.2 μ A range for 50 mV to 150 mV bias voltage around the nano-pads Fermi level.

References:

- [1] N. Renaud and C. Joachim, *Phys. Rev. A*, (2008), 78, 062316.
- [2] W. H. Soe et al; *Phys. Rev. B: Condens. Matter*, (2011), 83, 155443.
- [3] M. Kolmer, et al; *Nanoscale* (2015), 7, 12325–12330.
- [4] G. Dridi, R. Julien, M. Hliwa and C. Joachim, *Nanotechnology*, (2015), 26, 344003.
- [5] Namarvar, O. F. et al. ; *Sci. Rep.* 6, 30198; doi: 10.1038/srep30198 (2016).

Wednesday, 18th Esfand(1395) (8th March, 2017), 14:00-15:00

Farmaniyeh seminar room